

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 10-24-2003
Application Type:: Regular
Subject Matter:: Utility
Title:: Method and Circuit for Folded
Analog-to-Digital Converter (ADC)
Using Frequency Detectors and Time
Detectors
Attorney Docket Number:: X-1064-1D US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 07
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Michael
Middle Name:: A.
Family Name:: Nix
Street:: 16008 Scenic Oak Trail
City:: Buda
State or Province:: Texas
Postal or Zip Code:: 78610

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: Jordan
Status:: Full Capacity
Given Name:: Ahmed
Family Name:: Younis
Street:: 4701 Monterey Oaks Blvd, #1138

City:: Austin
State or Province:: Texas
Postal or Zip Code:: 78749

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Applic.	Divisional	10/224,976	08-20-02

ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124